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Title of Invention	SiGe HETEROJUNCTION BIPOLAR TRANSISTOR (HBT) AND METHOD OF FABRICATION	
Application Number :		
Date :		
First Named Applicant:	Peter J. Geiss	
Confirmation Number:		
Attorney Docket Number:	BUR920040004US1	
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Documents being submitted:	Files
us-assignment	BUR920040004US1-usassn.xml us-assignment.xsl us-assignment.dtd BUR920040004Assign1.tif BUR920040004Assign2.tif
us-request	BUR920040004US1-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	BUR920040004US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	BUR920040004dec1.tif
us-declaration	BUR920040004dec2.tif
application-body	BUR920040004FINALABX.xml application-body.dtd Image1.tif Image2.tif Image3.tif isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isogr1.ent isogr2.ent isogr3.ent isogr4.ent isolat1.ent isolat2.ent isomfrk.ent isomopf.ent isomscr.ent isonum.ent isopub.ent isotech.ent mathml2.dtd mathml2-qname-1.mod mmlalias.ent mmlextra.ent soextblx.dtd us-application-body.xsl wipo.ent
application-body-pdf-wrap	BUR920040004FINALABX-pdf-wrap.xml
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Comments	
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